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of the issues in co-design such as **hardware-software co-simulation** [9] 15]One of the most pressing
language would facilitate seamless **hardware-software co-simulation**. Moreover, a single language would
www.bib.informatik.tu-muenchen.de/cdviews/dac97/papers/1997/dac97/htmlfiles/sun_sgi/./psfiles/03_4.ps

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simulation speedups. 1 Introduction **Hardware-software co-simulation** is used to validate both the
give better feedback. 7 Conclusion **Hardware-software co-simulation** of embedded systems can perform
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tools for code generation and **hardware-software co-simulation** have become essential parts of the
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estimation, coupled with a fast **hardware/software co-simulation** framework, is a key enabler to
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: 48 6 **Hardware-software co-simulation** 51 6.1 Co-simulation and
than via mathematical analysis. **Hardware /software co-simulation** is generally performed with separate
is not implemented either. 50 6 **Hardware-software co-simulation** This section describes the various
ic.eecs.berkeley.edu/pub/HWSW/polis_man.0.4.ps.gz

[Efficient Power Estimation Techniques for HW/SW Systems](#) - Marcello Lajolo (1999) (Correct) (2 citations)
estimates. However, as in the case of **hardware/software co-simulation**, the communication and
emission of an event, etc. Prior to **hardware/software co-simulation**, the software parts of the system
65-70, Mar. 1998. 15] J. Rowson. **Hardware/software co-simulation**. In Proc. Design Automation Conf.
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[Models and Methods for HW/SW Intellectual Property..](#) - Ortega, Lavagno, Borriello (1998) (Correct) (3 citations)
we discuss various strategies for **hardware/software co-simulation**, with special attention to the
validation issues, in terms of both **hardware-software co-simulation**, and of formal verification. 1.1
www-cad.eecs.berkeley.edu/Respep/Research/hsc/class/ee249/papers/asi98_submitted.ps.gz

[Trends in Embedded Systems Technology: An Industrial Perspective](#) - Pierre Paulin (1995) (Correct) (4 citations)
commercial processor core. 3. CoGen, a **hardware-software co-simulation** interface generator. DRAFT: NATO
cwc.ucsd.edu/courses/billlin/S97/ece260C/reading/nato95.ps.gz

[The Design of Mixed Hardware/Software Systems](#) - Adams, Thomas (1996) (Correct) (4 citations)
tasks hardware-software partitioning **hardware-software co-simulation** system design hardware-software
into every subset of this diagram. 3.1 **Hardware/software co-simulation** Simulation of hardware/software
systems, sometimes called **hardware -software co-simulation**, presents the problem of modeling
jerry.c-lab.de/~wolfgang/TUTORIALS/96/34_1.ps.Z

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is quite recent. This started with **hardware software co-simulation** [4]5]9]3]and now it is
Hagen and H. Meyr. Timed and untimed **hardwaresoftware co-simulation**: Application and efficient
1993. 5] S. Lee and J. Rabaey. A **hardware-software cosimulation** environment. International Workshop
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Architecture Description Languages for Systems-on-Chip .. - Tomiyama, Halambi.. (1999) (Correct) (1 citation)
of hardware, debugging of software, **hardware/software co-simulation**, and so on. A variety of simulators
in the ADL specification)Then, **hardware/software co-simulation** is performed using the generated
www.cecs.uci.edu/~aces/Pubs/apchdl99.ps

Pia: A Framework for Embedded System Co-simulation with Dynamic .. - Hines (1996) (Correct) (2 citations)
simulation speedups. 1 Introduction **Hardware-software co-simulation** is used to verify the correctness of
give better feedback. 7 Conclusion **Hardware-software co-simulation** of embedded systems can perform
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a simulator of the hardware portions (**hardware/software co-simulation**)However, exploring software
16-28, Sept. 1993. 2] J. Rowson, **Hardware/Software co-simulation**, in 31st ACM/IEEE Design
www.ert.rwth-aachen.de/Projekte/Tools/COMPILED_SIMULATION/./PAPERS/cad_Zivojnovic95vlsi.ps.gz

An Engineering Environment for Hardware/Software Co-Simulation - Becker, Singh, Tell (1992) (Correct) (1 citation)
An Engineering Environment for **Hardware/Software Co-Simulation** David Becker, Raj K. Singh, Stephen
al. An Engineering environment for **Hardware-Software Co-Simulation** In Proceedings of the 29th
129-134 An Engineering Environment for **Hardware/Software Co-Simulation** David Becker, Raj K. Singh, Stephen
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statically. In addition, a traditional **hardware/software co-simulation** stage is required in order to
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Sesame does not perform traditional **hardware/software co-simulation** [5] in which the software and
Design, November 2001. 5] J. Rowson. **Hardware/software co-simulation**. In Proc. of the Design Automation
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statically. In addition, a traditional **hardware/software co-simulation** stage is required in order to
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software development toolchain. **Hardware-software co-simulation** with the 8051simulation model,
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